



Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete if Known</b> Application Number 10/813,461 Filing Date March 30, 2004 First Named Inventor Tsai, Vicki Art Unit 2825 Examiner Name <del>Unknown</del> Suchin Parihar	
Sheet	1	of	1	Attorney Docket No: 80107.116US1	

US PATENT DOCUMENTS					
Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date if Appropriate
SP		US-7073159	07/04/2006	Tsai, V. W., et al.	03/31/2004

FOREIGN PATENT DOCUMENTS					
Examiner Initials *	Cite No	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T <sup>2</sup>
SP		EP-1315335A1	05/28/2003	Cimini, Jr., L. J., et al.	
SP		WO-05098685A2	10/20/2005	Tsai, V. , et al.	
SP		WO-2005098685A3	10/20/2005	Tsai, V. , et al.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS					T <sup>2</sup>
Examiner Initials *	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
SP		BENINI, L. , et al., "System-Level Power Optimization: Techniques and Tools", ACM Transactions on Design Automation of Electronic Systems, 5(2), (Apr. 2000), 115-192			
SP		TSUI, E. , et al., "A New Distributed DSP Architecture Based on the Intel IXS for Wireless Client and Infrastructure", Hot chips 14, Conf. record Session 7: digital signal processors, 20 Aug. 2002, (2002), 1-17			

EXAMINER

/Suchin Parihar/

DATE CONSIDERED

01/26/2007